

**PATENT**  
**(5580-02102/BP1829DIV)**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

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Application No.: Unknown  
Filed: Herewith  
Inventor(s):  
Joseph M. Ingino, Jr.

Title: HIGH BANDWIDTH,  
HIGH PSRR, LOW  
DROPOUT VOLTAGE  
REGULATOR


§ Examiner: Unknown  
§ Group/Art Unit: Unknown  
§ Atty. Dkt. No: 5580-02102  
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CERTIFICATE OF EXPRESS MAIL  
UNDER 37 C.F.R. §1.10

"Express Mail" mailing label number: EV 324268466 US  
DATE OF DEPOSIT: July 9, 2003

I hereby certify that this paper or fee is being deposited with the  
United States Postal Service "Express Mail Post Office to  
Addressee" service under 37 C.F.R. §1.10 on the date indicated  
above and is addressed to:

Commissioner for Patents  
M/S Patent Application  
P.O. Box 1450  
Alexandria, VA 22313-1450

  
Derrick Brown

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**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
M/S Patent Application  
P.O. Box 1450  
Alexandria, VA 22313-1450  
Sir:

Applicant requests consideration of ☒ the references listed on the attached Form PTO-1449 and/or ☒ the additional information identified below in paragraph 3. A copy of some of the references list on the From PTO-1449 are attached. Those listed and not attached were previously cited in application serial no. 09/833,299 to which the captioned application is seeking priority under 35 U.S.C. § 120; therefore, a copy of each reference is not enclosed.

1. This Information Disclosure Statement is submitted:

- a. ☒ within 3 months of the filing date of the above identified application, or within 3 months of entry into National stage prosecution of an International application, or before the mailing

date of the first Office Action on the merits, and thus no certification and/or fee is required.

- b. ☐ after the events of above paragraph 1a and prior to the mailing date of a final Office Action or Notice of Allowance, and thus:
- ☐ the certification of paragraph 2 below is provided, or  
☐ a fee authorization for \$180.00 is enclosed.
- c. ☐ after the mailing date of a final Office Action or a Notice of Allowance and prior to payment of the issue fee, and thus:
- ☐ the certification of paragraph 2 below is provided, and  
☐ a fee authorization for \$180.00 is enclosed.

2. It is hereby certified:

- ☐ that each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the Statement, or
- ☐ that no item of information contained in the Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the person signing the certification after making reasonable inquiry, was known to any individual designated in § 1.56 (c) more than three months prior to the filing of the Statement.

3. ☒ Consideration of the following additional information (including any co-pending or abandoned U.S. applications, prior uses and/or sales, etc.) is requested:

U.S. Patent Application Serial No. 10/404,893	(5580-01803)
U.S. Patent Application Serial No. 10/253,274	(5580-01902)
U.S. Patent Application Serial No. 10/191,218	(5580-02002)

pp. 1-3, entitled "Background of the Invention," from the captioned application specification

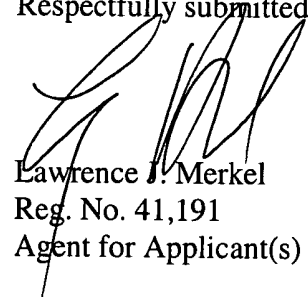
4. For each non-English language reference listed on the attached Form PTO-1449:

- ☐ reference is made to an English language translation submitted herewith, and/or
- ☐ reference is made to a foreign patent office search report (in the English language) submitted herewith, and/or

- ☐ reference is made to an English language translation of a foreign patent office search report submitted herewith, and/or
- ☐ reference is made to the concise explanation contained in the specification of the present application at page(s) \_\_\_\_\_, and/or
- ☐ reference is made to the concise explanation set forth below:
5. ☐ Applicant also offers the following comments for the Examiner's consideration:
6. ☐ Also enclosed is a copy of a foreign search report citing these references.
7. ☐ The listed documents were brought to the attention of the Applicant(s) after payment of the issue fee in the captioned case. The documents were cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement. Applicant(s) request this Information Disclosure Statement and attached Form PTO-1449 be placed in the file of the captioned application.
8. ☐ Applicant(s) requests that the Information Disclosure Statement and attached Form PTO-1449 and references, which are being filed before the grant of the patent and pursuant to 37 C.F.R. § 1.97(i), be placed in the file of the captioned application.

If any required fees are missing, the Commissioner is authorized to charge said fees to Meyertons, Hood, Kivlin, Kowert & Goetzel, P.C. Deposit Account No. 50-1505/5580-02102/LJM.

Respectfully submitted,

  
Lawrence J. Merkel  
Reg. No. 41,191  
Agent for Applicant(s)

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Austin, TX 78767-0398  
(512) 853-8800

Date: July 9, 2003

<b>Form PTO-1449</b> (modified) List of Patents and Publications For Applicant's Information Disclosure Statement (Use several sheets if necessary)	ATTY. DKT. NO. 5580-02102  APPLICANT: Joseph M. Ingino, Jr.  FILING DATE: Herewith	SERIAL NO. Unknown  GROUP: Unknown
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**U.S. PATENT DOCUMENTS**

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
		5,151,665	9/29/92	Wentzler			
		5,345,119	9/6/94	Khoury			
		5,559,474	9/24/96	Matsumoto, et al.			
		5,631,587	5/20/97	Co, et al.			
		5,977,806	11/2/99	Kikuchi			
		6,127,880	10/3/00	Holst, et al.			
		5,404,250	4/4/95	Hase, et al.			
		6,037,622	3/14/00	Lin, et al.			
		6,046,626	4/4/00	Saeki, et al.			
		5,898,336	4/27/99	Yamagushi			
		6,320,470	11/20/01	Arai, et al.			
		5,512,861	4/30/96	Sharma			
		5,485,126	1/16/96	Gersbach, et al.			
		6,441,660	8/27/02	Ingino, Jr.			
		6,483,358	11/19/02	Joseph Ingino, Jr.			
		5,130,883	7/92	Edwards			
		3,908,159	9/75	Griffey			
	A1	6,566,970	5/20/02	Joseph Ingino, Jr.			

**FOREIGN PATENT DOCUMENTS**

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES/NO
		282 673	9/21/88	EP			

EXAMINER:

DATE CONSIDERED:

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the patent owner.

<b>Form PTO-1449</b> (modified) List of Patents and Publications For Applicant's Information Disclosure Statement (Use several sheets if necessary)			ATTY. DKT. NO. 5580-02102  APPLICANT: Joseph M. Ingino, Jr.  FILING DATE: Herewith		SERIAL NO. Unknown  GROUP: Unknown		
<b>U.S. PATENT DOCUMENTS</b>							
EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
<b>OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)</b>							
		Young, et al., "A PLL Clock Generator with 5 to 110 MHz of Lock Range for Microprocessors," IEEE Journal of Solid-State Circuits, Vol. 27, No. 11, November 1992, 9 pages.					
		Vincent R. von Kaenel, "A High-Speed, Low-Power Clock Generator for a Microprocessor Application," IEEE Journal of Solid-State Circuits, Vol. 33, NO. 11, November 1998, 3 pages.					
		Patrik Larsson, Bell Labs, Lucent Technologies, Holmdel, NJ, "A2-1600MHz 1.2-2.5V CMOS Clock-Recovery PLL with Feedback Phase-Selection and Averaging Phase-Interpolation for Jitter Reduction," IEEE International Solid-State Circuits Conference, ISSCC99/Session 20/Paper WA 20.6, 1999, 1 page.					
		Bult, et al., "A Fast-Settling CMOS Op Amp for SC Circuits with 90-dB DC Gain," IEEE Journal of Solid-State Circuits, Vol. 25, NO. 6, December 1990, 3 pages.					
		von Kaenel, et al., "A 320 MHz, 1.5 mW @ 1.35 V CMOS PLL for Microprocessor Clock Generation," IEEE Journal of Solid-State Circuits, Vol. 31, No. 11, November 1996, 4 pages.					
		Horowitz, "The Art of Electronics," Cambridge Univ. Press, 1980 15 pages.					
		SiByte, "Target Applications," <a href="http://sibyte.com/mercurian/applications.htm">http://sibyte.com/mercurian/applications.htm</a> , January 15, 2001, 2 pages.					
		SiByte, "SiByte Technology," <a href="http://sibyte.com/mercurian/technology.htm">http://sibyte.com/mercurian/technology.htm</a> , January 15, 2001, 3 pages.					
		SiByte, "The Mercurian Processor," <a href="http://sibyte.com/mercurian">http://sibyte.com/mercurian</a> , January 15, 2001, 2 pages.					
		SiByte, "Fact Sheet," SB-1 CPU, October 2000, rev. 0.1, 1 page.					
		SiByte, "Fact Sheet," SB-1250, October 2000, rev. 0.2, 10 pages.					
		Stepanian, SiByte, "SiByte SB-1 MIPS64 CPU Core, Embedded Processor Forum 2000, June 13, 2000, 15 pages.					
		Jim Keller, "The Mercurian Processor: A High Performance, Power-Efficient CMP for Networking," October 10, 2000, 22 pages.					
		Tom R. Halfhill, "SiByte Reveals 64-Bit Core For NPUs; Independent MIPS64 Design Combines Low Power, High Performance," Microdesign Resources, June 2000, Microprocessor Report, 4 pages.					

EXAMINER:

DATE CONSIDERED:

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the patent owner.

Information Disclosure Statement--PTO 1449 (modified)